



PATENT APPLICATION
Docket No. 8750-044
Client No. SPX200208-0018US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Mun-Mo Jeong, et al. Conf. No. 2385
Serial No.: 10/636,131 Examiner: not yet assigned
Filing Date: August 6, 2003 Art Unit: 2811
Title: BITLINE OF SEMICONDUCTOR DEVICE HAVING STUD TYPE CAPPING
LAYER AND METHOD FOR FABRICATING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (b); or
(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last)
☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97 (e), or
☐ a \$180 fee under 37 CFR 1.17 (p); or
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office

Action)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Customer No. 20575

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.

Alan T. McCollom
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
503-222-3613

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
Date: January 29, 2004

Adrienne Chocholak



PATENT APPLICATION
Docket No. 8750-044
Client No. SPX200208-0018US

Inventor(s): Mun-Mo Jeong, et al. Conf. No. 2385
Serial No.: 10/636,131 Examiner: not yet assigned
Filing Date: August 6, 2003 Art Unit: 2811
Title: BITLINE OF SEMICONDUCTOR DEVICE HAVING STUD TYPE CAPPING
LAYER AND METHOD FOR FABRICATING THE SAME

INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

FOREIGN PATENT DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	Document <u>Number</u>	Publication <u>Date</u>	<u>Country</u>	<u>Name</u>
_____	_____	2001-55685	7/4/2001	Korea	

OTHER DOCUMENTS

Exam <u>Init</u>	<u>Ref</u>	<u>Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	English language of Abstract for Korean Publication No. 2001-55685, published on 7/4/2001.

Examiner: _____

Date Considered: _____